

FIG. 1

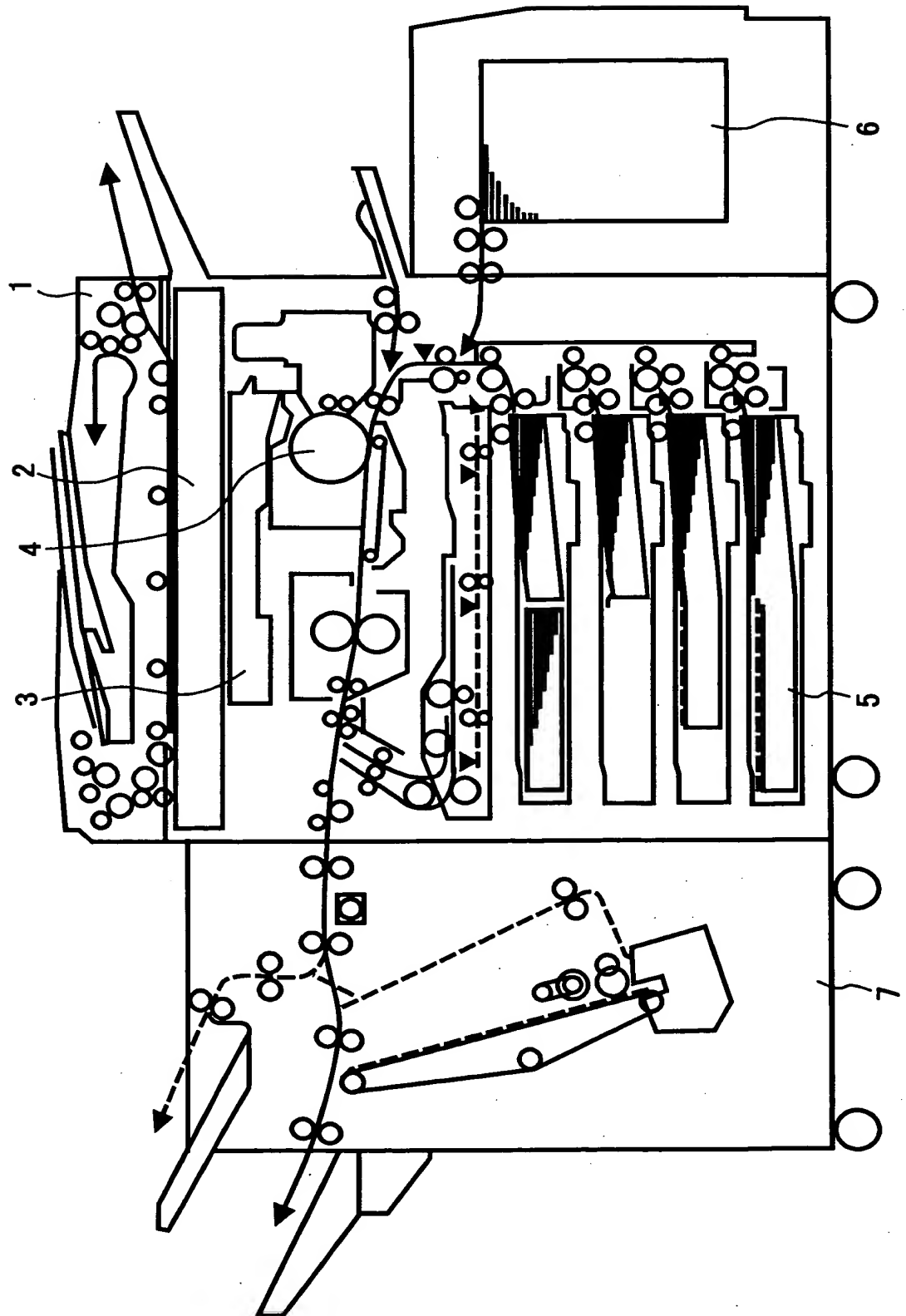
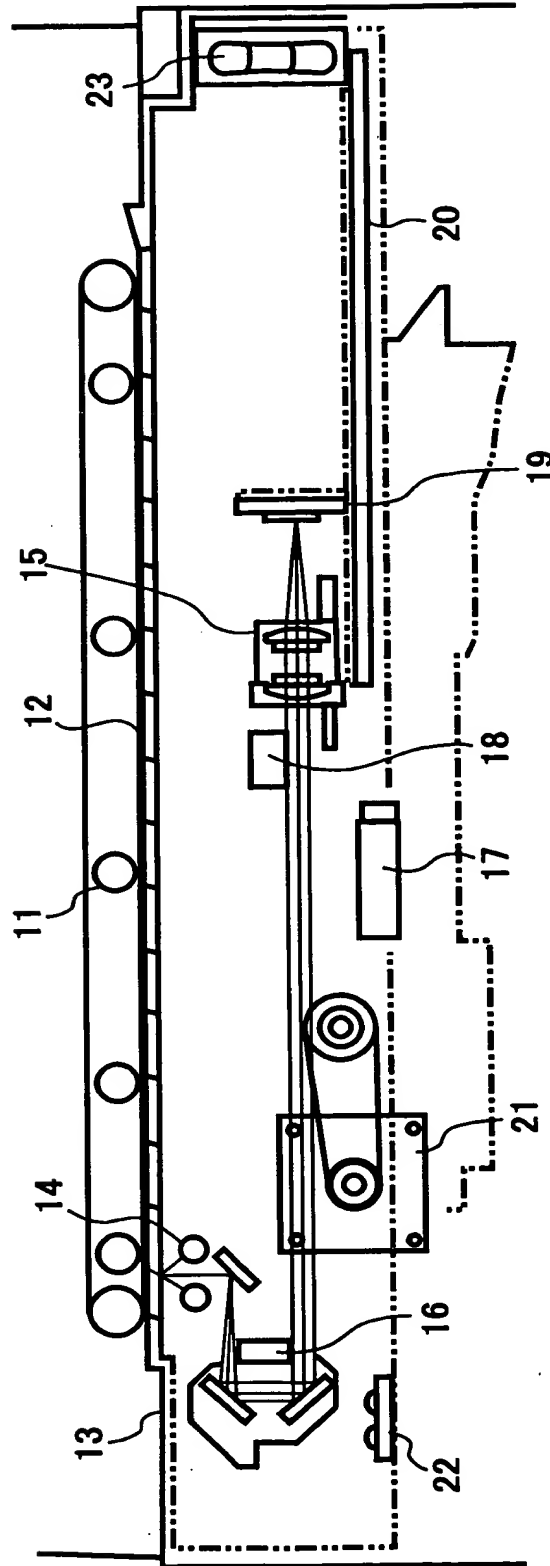


FIG. 1

FIG. 2



The diagram shows a **MAIN CONTROL BOARD 10** at the top, containing an **MPU**. It is connected to a **CPU 30** via a bus 125. The CPU 30 is connected to an **ADF 1** and a **memory 113**. A **display 19** is connected to the CPU 30 through a **driver 21** and a **sensor 20**. The main control board 10 is also connected to an **HDD 112** and a **LD 117**. A **mother board 120** is connected to the main control board 10 via a **connector 126** and a **bus 127**. The mother board 120 contains several components labeled 121, 122, 123, and 124. The main control board 10 is connected to an **I/O CONTROL BOARD** via a bus 116. The I/O CONTROL BOARD is connected to a **SHEET FEEDING CONTROL BOARD 118** and a **memory 104**. The sheet feeding control board 118 is connected to a **memory 105** and a **memory 106**. The I/O CONTROL BOARD is also connected to a **memory 114** and a **memory 115**.

FIG. 4

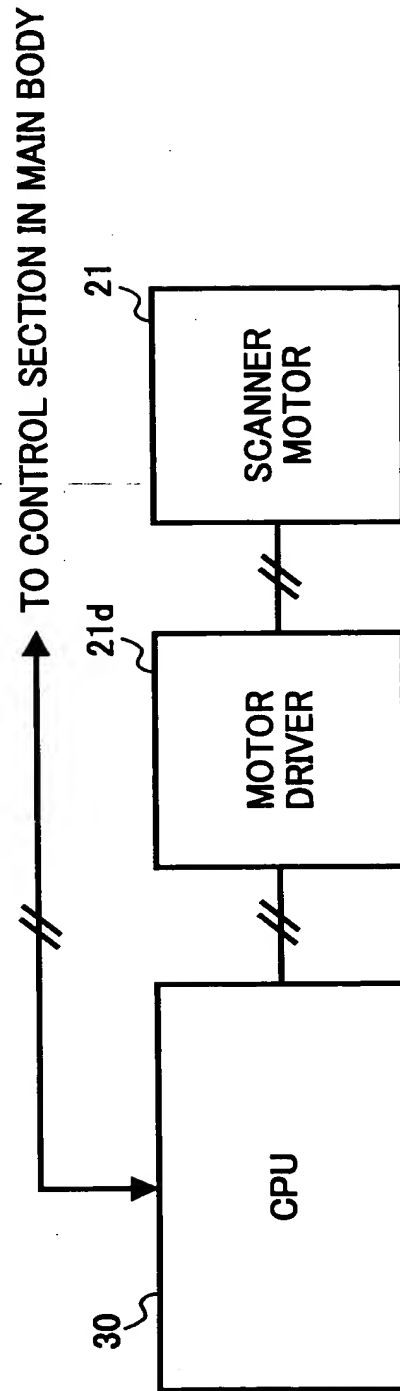
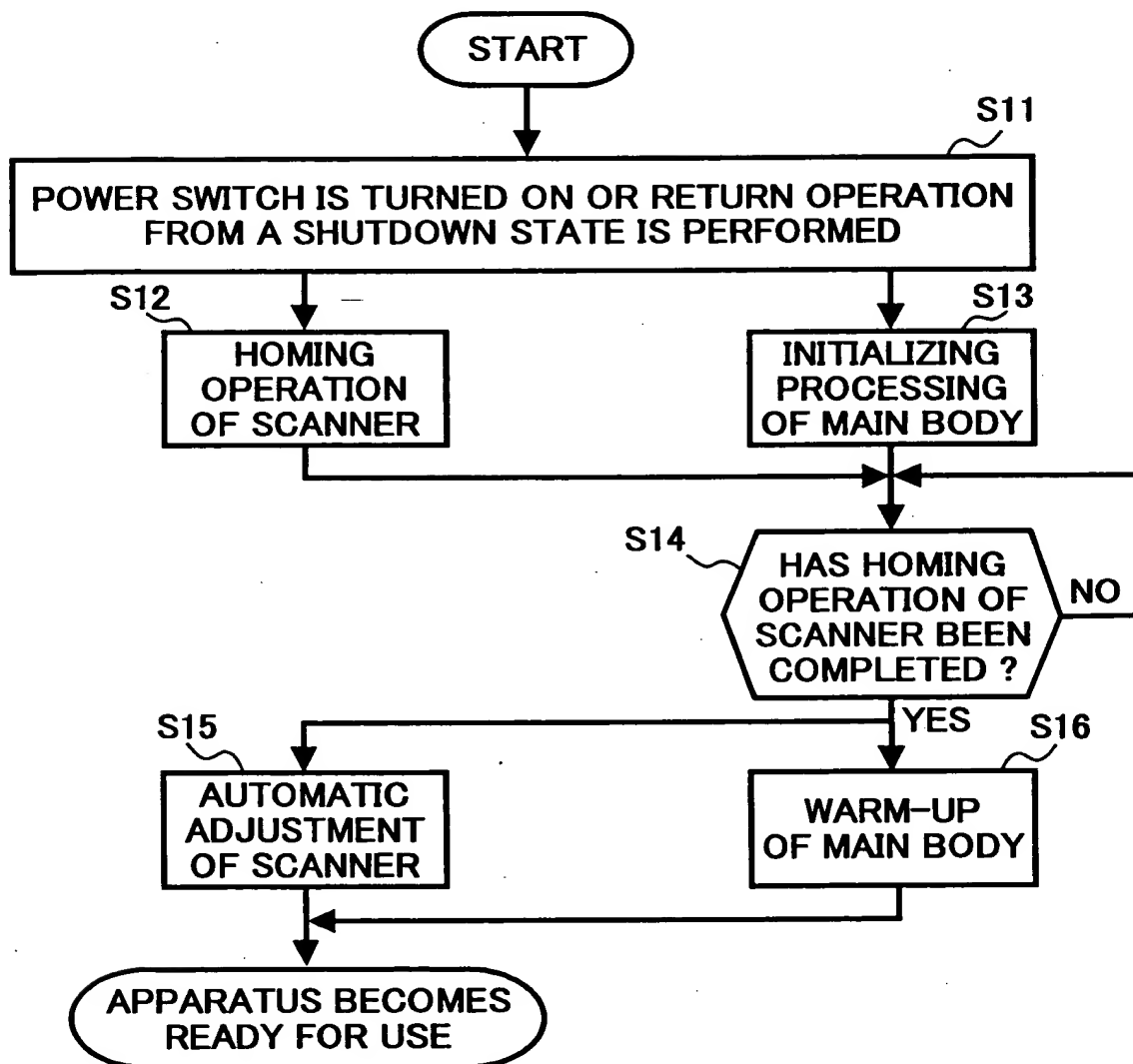


FIG. 5



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FIG. 6

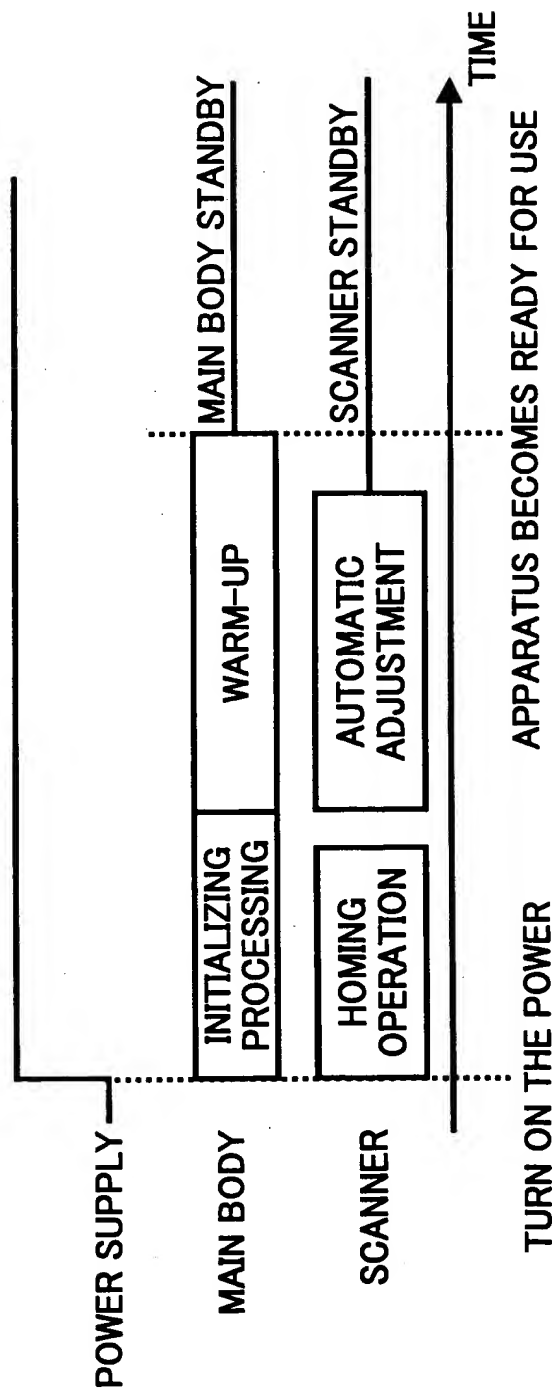


FIG. 7

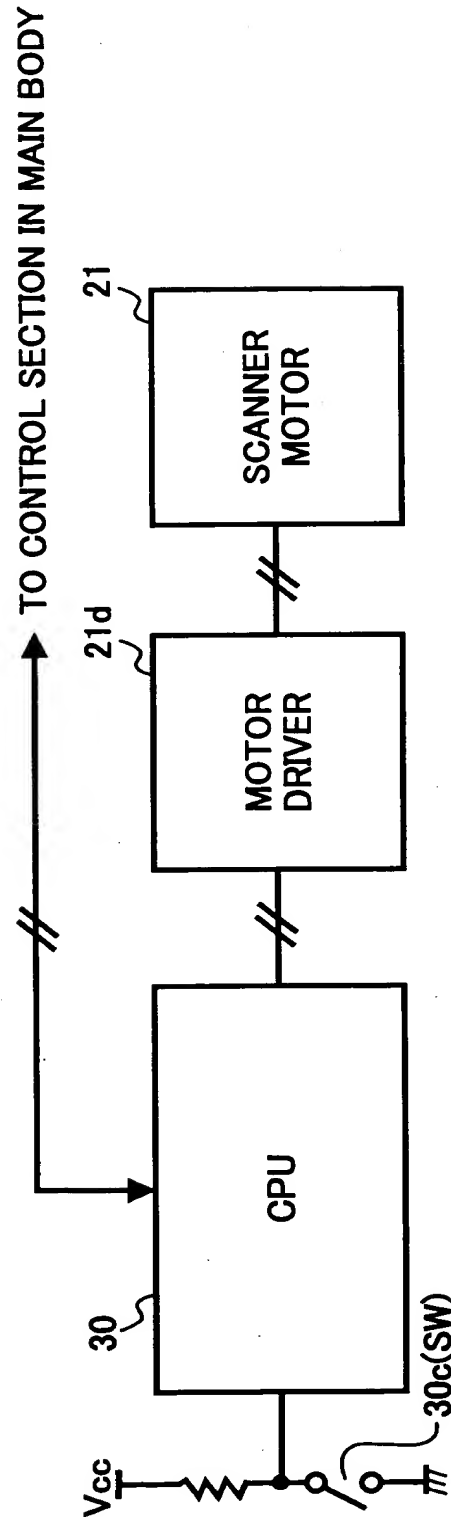
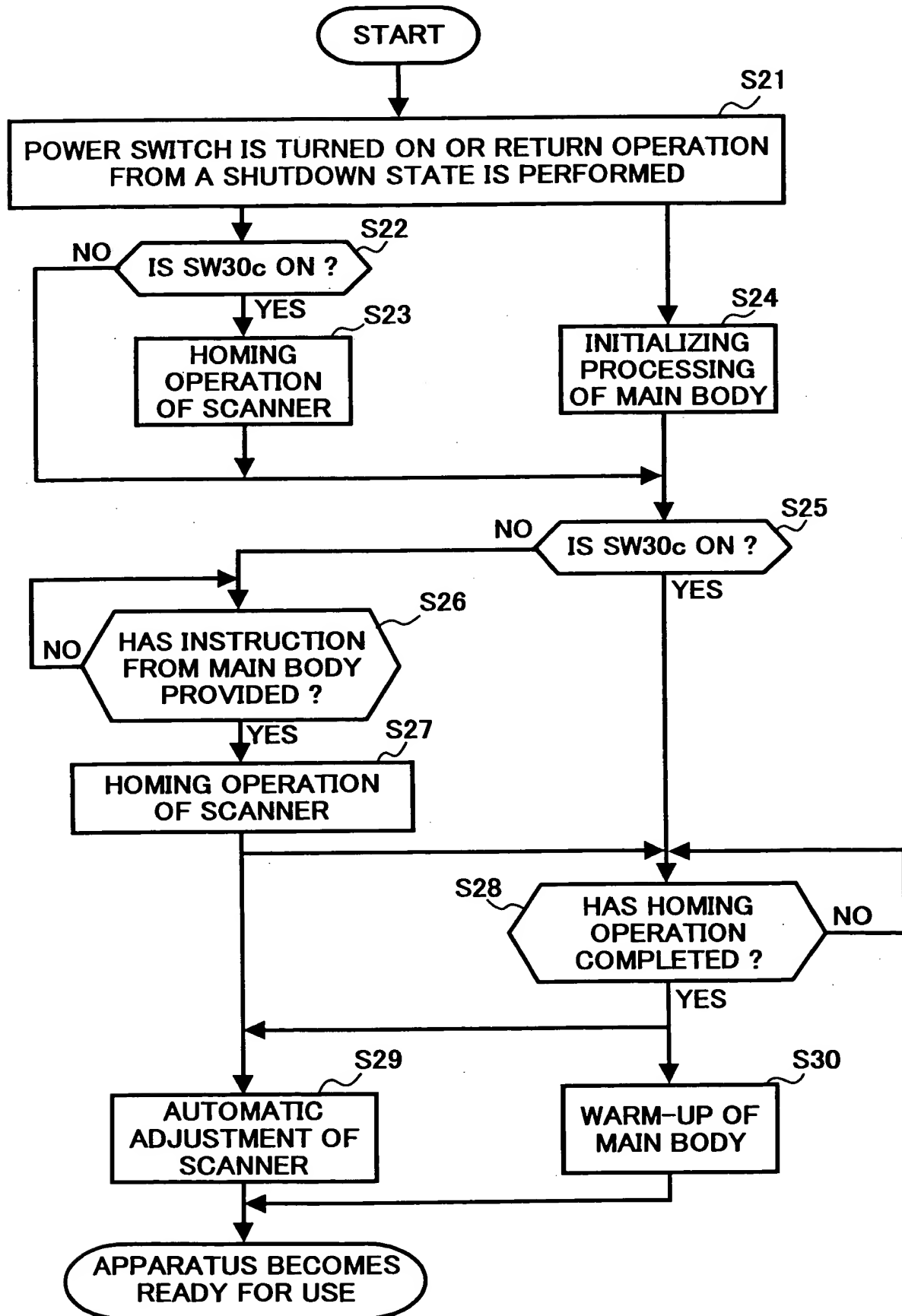


FIG. 8



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FIG. 9A WHEN SW30c IS ON

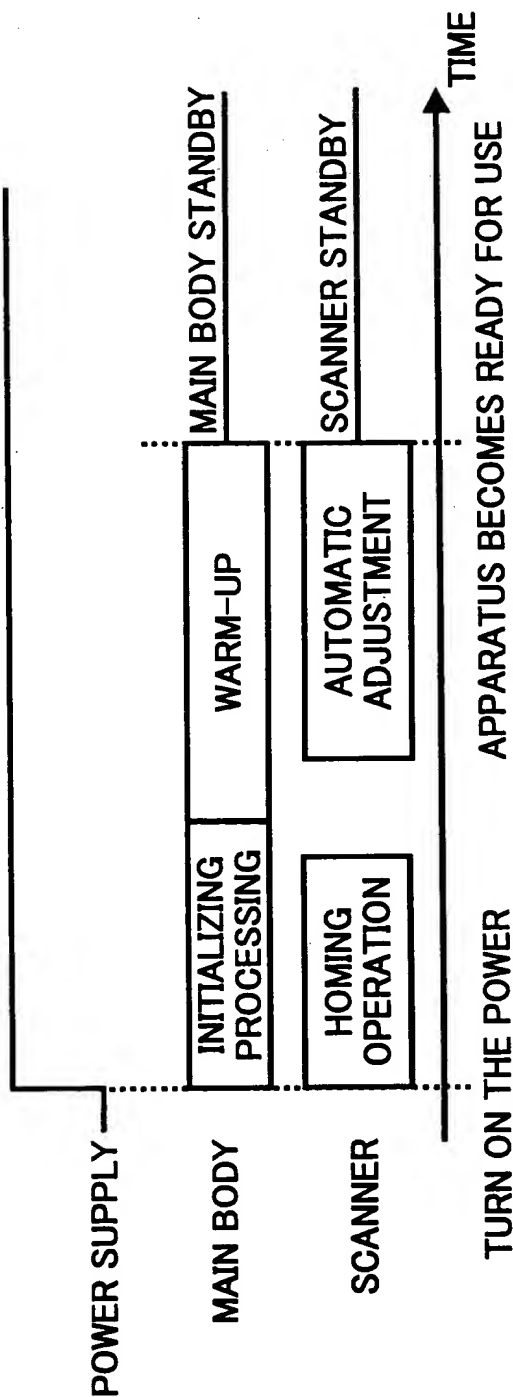


FIG. 9B WHEN SW30c IS OFF

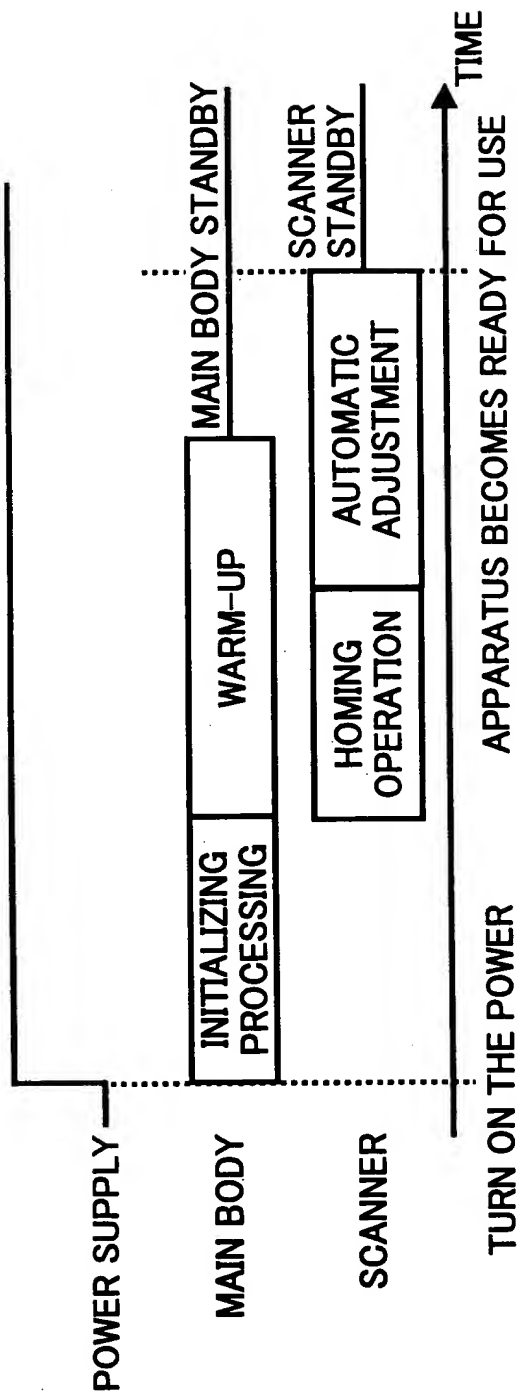


FIG. 10

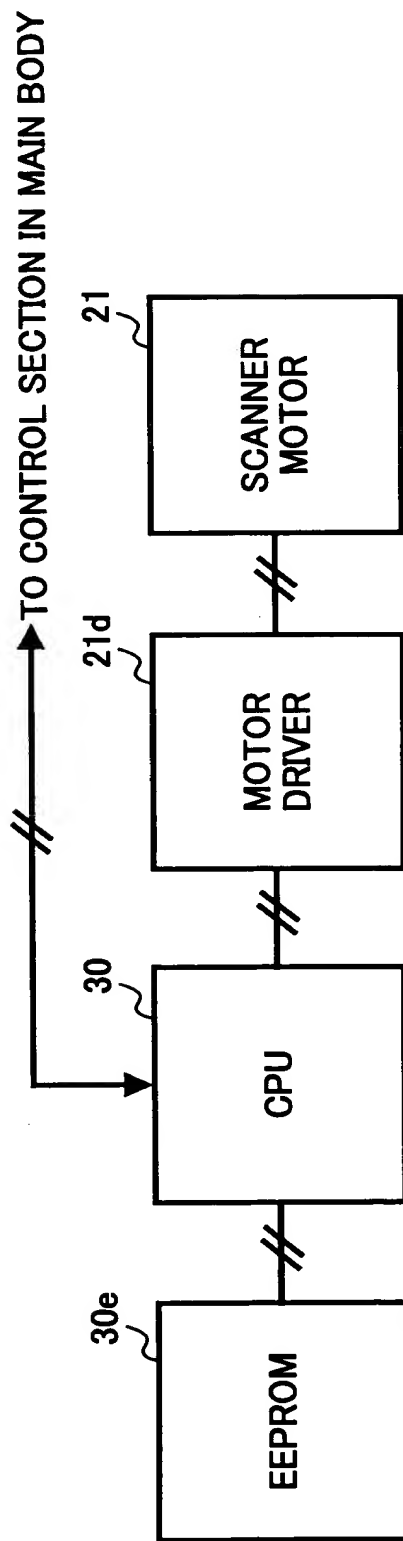
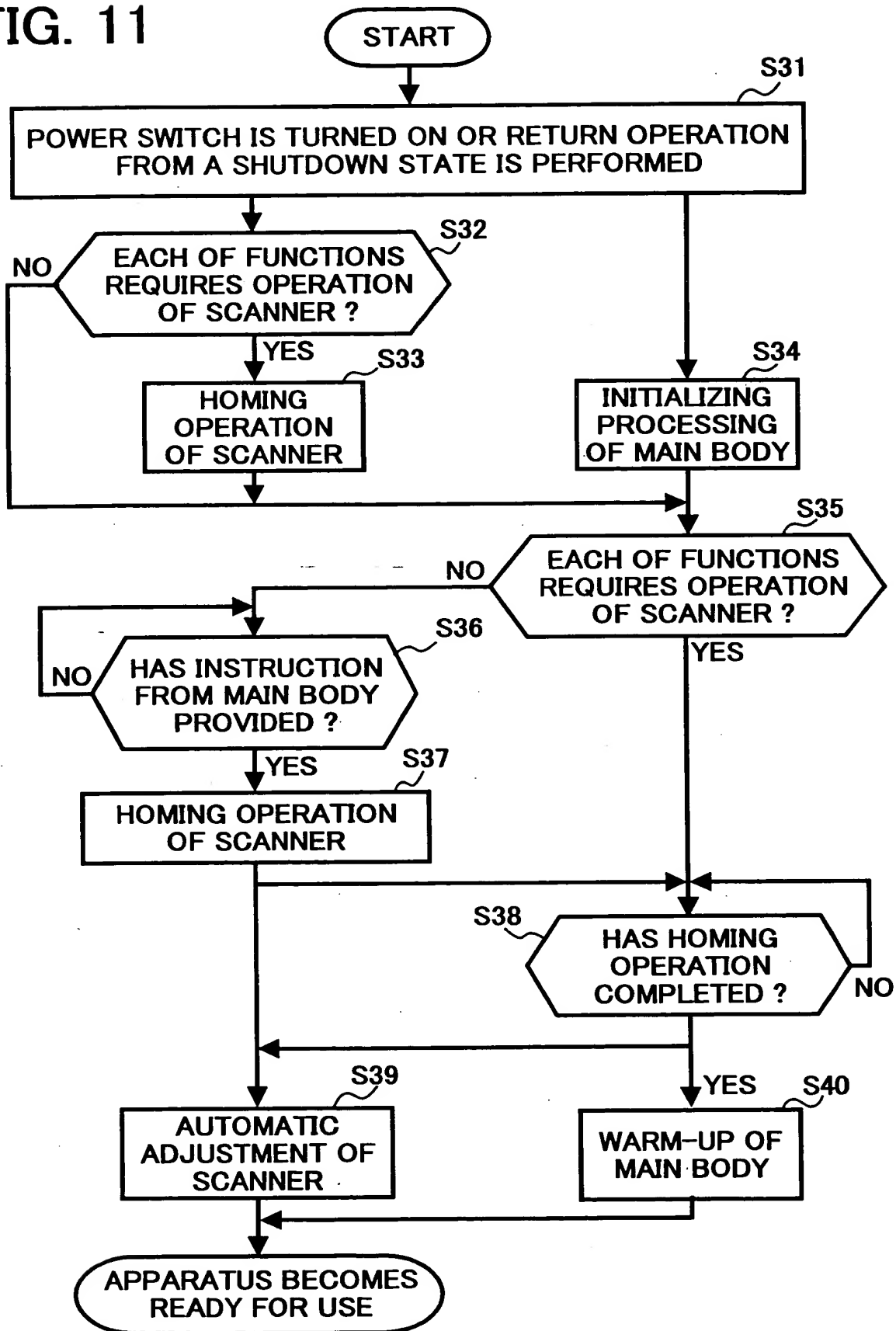


FIG. 11



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FIG. 12A

EACH OF FUNCTIONS REQUIRES OPERATION OF SCANNER

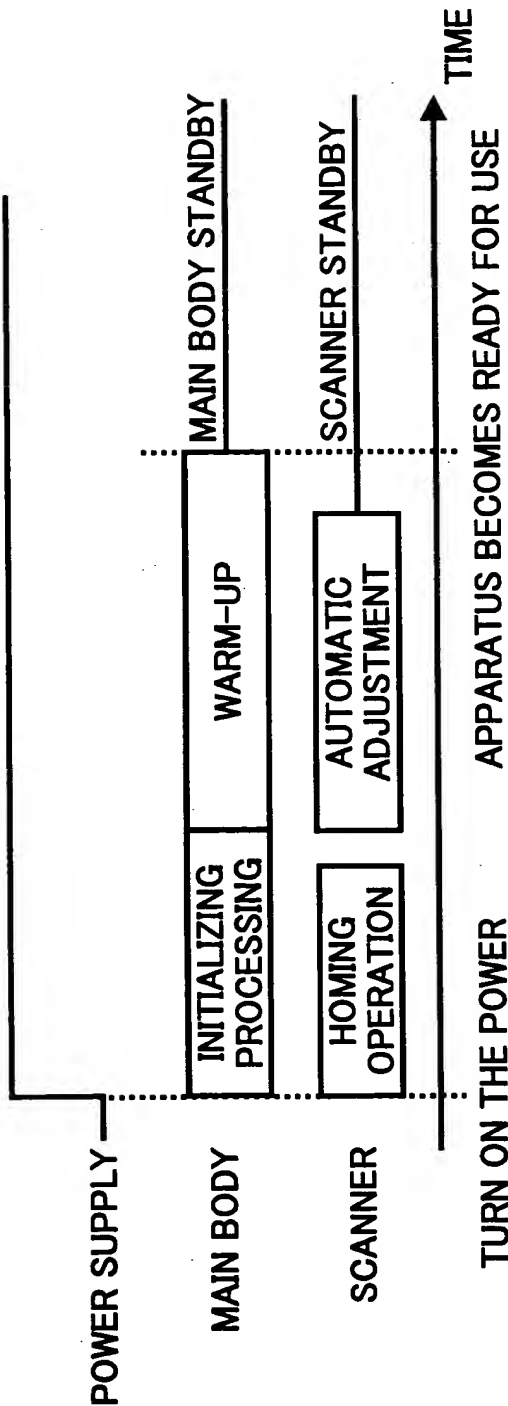


FIG. 12B

AT LEAST ONE OF FUNCTIONS DOES NOT REQUIRE OPERATION OF SCANNER

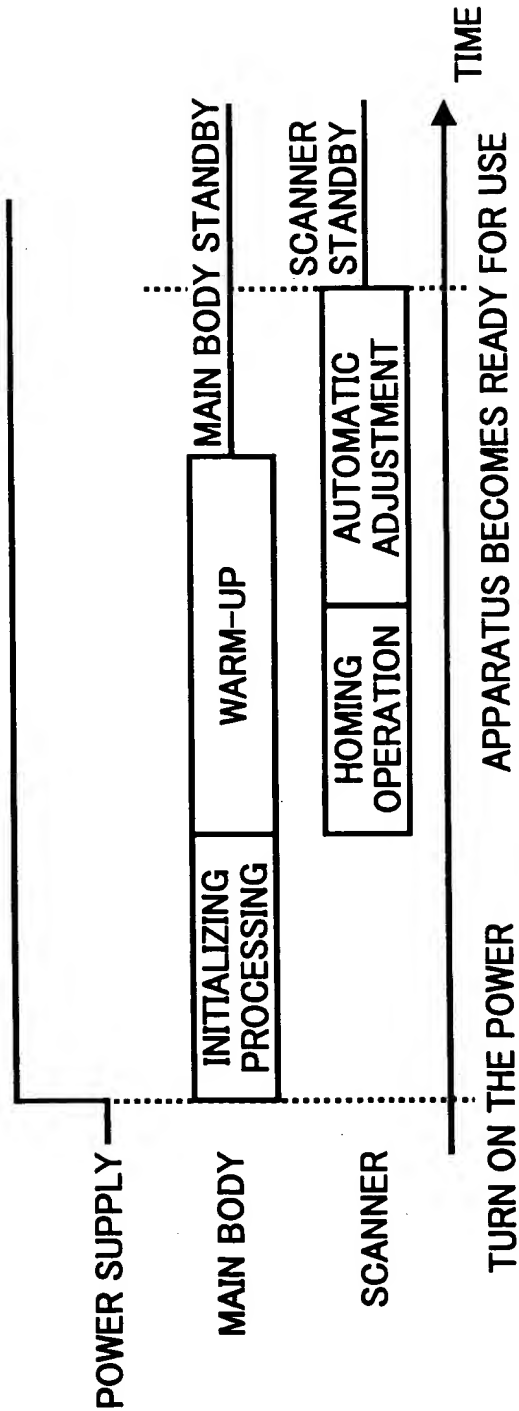


FIG. 13

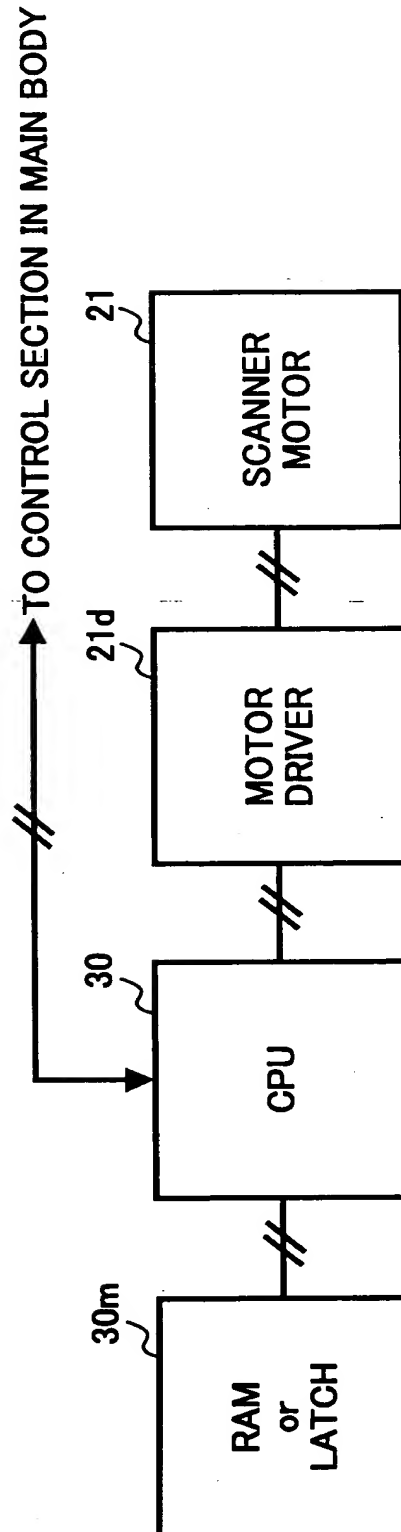
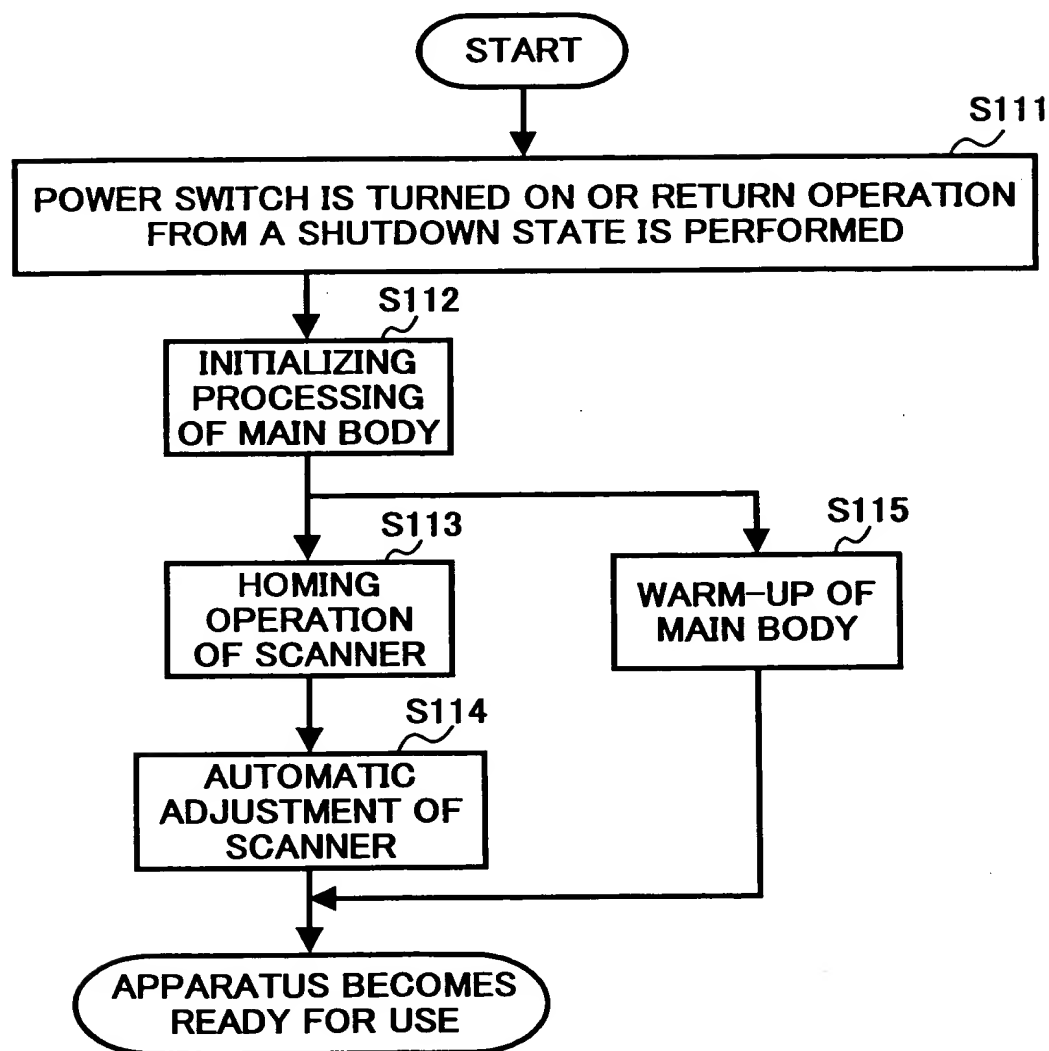


FIG. 14

BACKGROUND ART



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FIG. 15A BACKGROUND ART

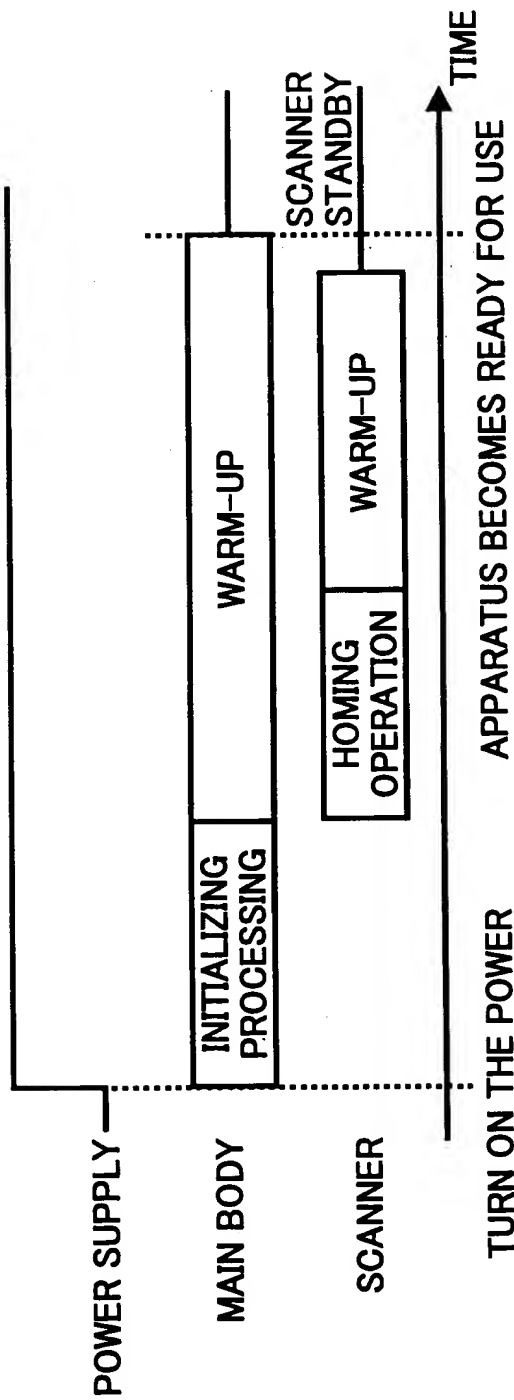


FIG. 15B BACKGROUND ART

